

on-chip inductor 10

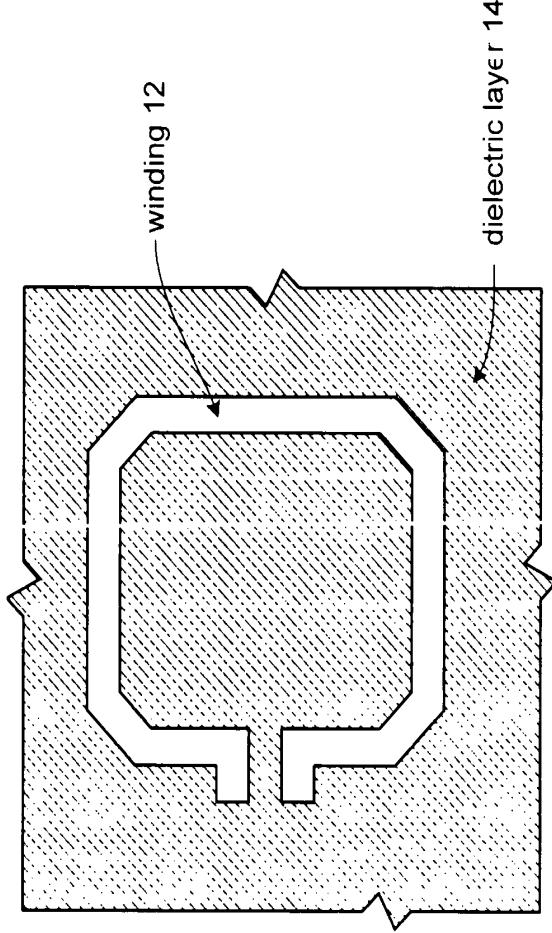


FIG. 1A
top view

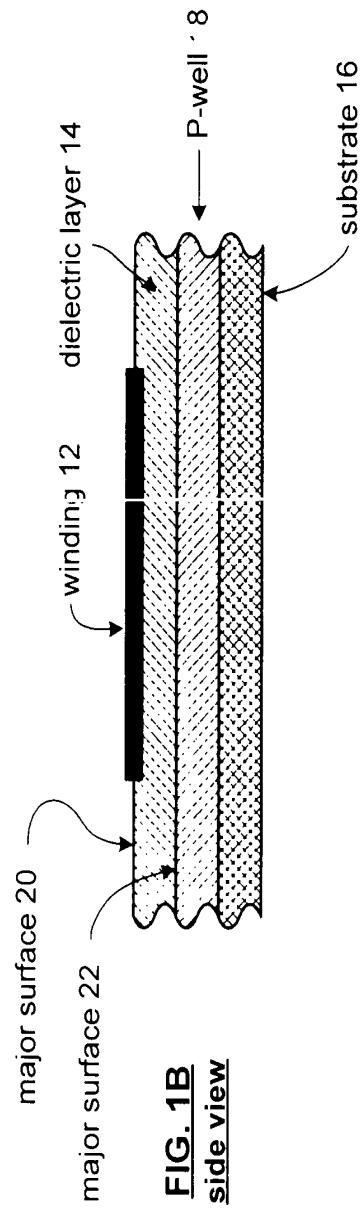


FIG. 1B
side view

on-chip inductor 25

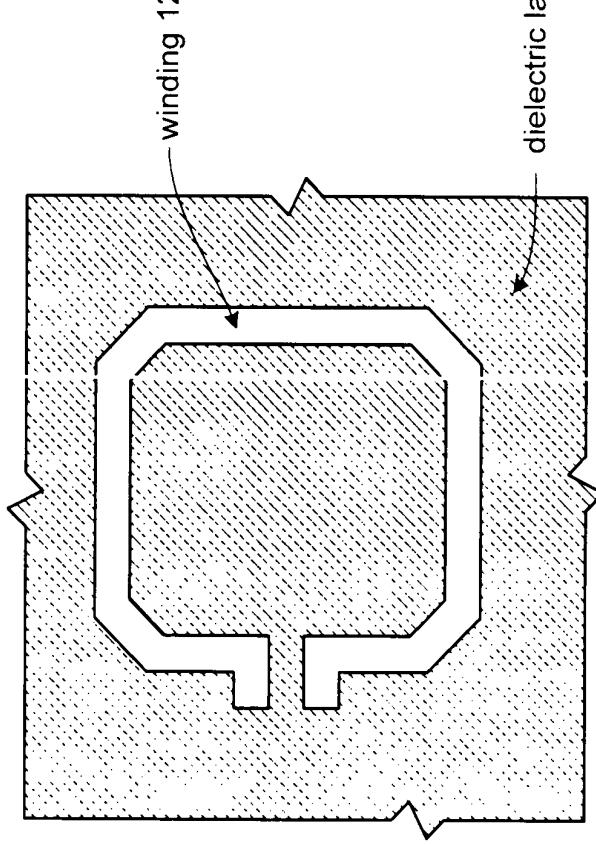


FIG. 2A
top view

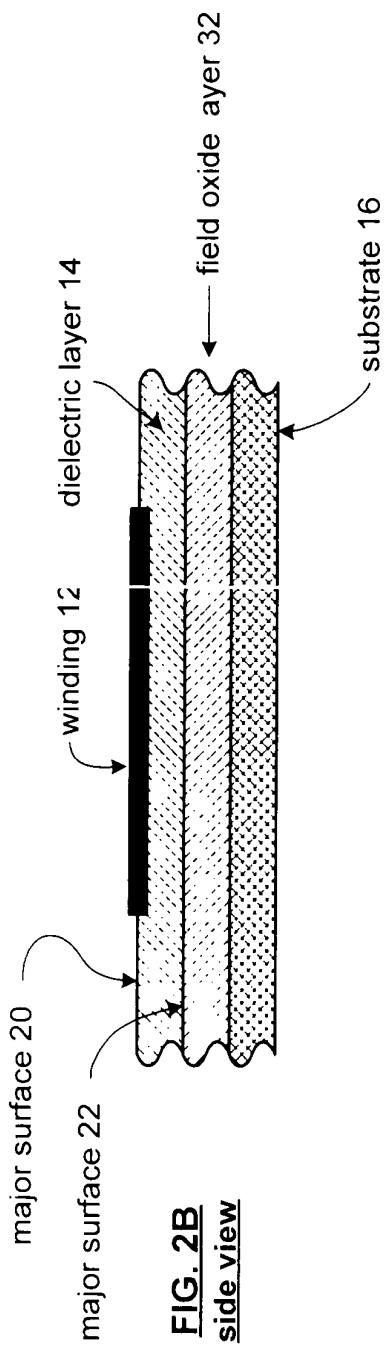


FIG. 2B
side view

on-chip inductor 30

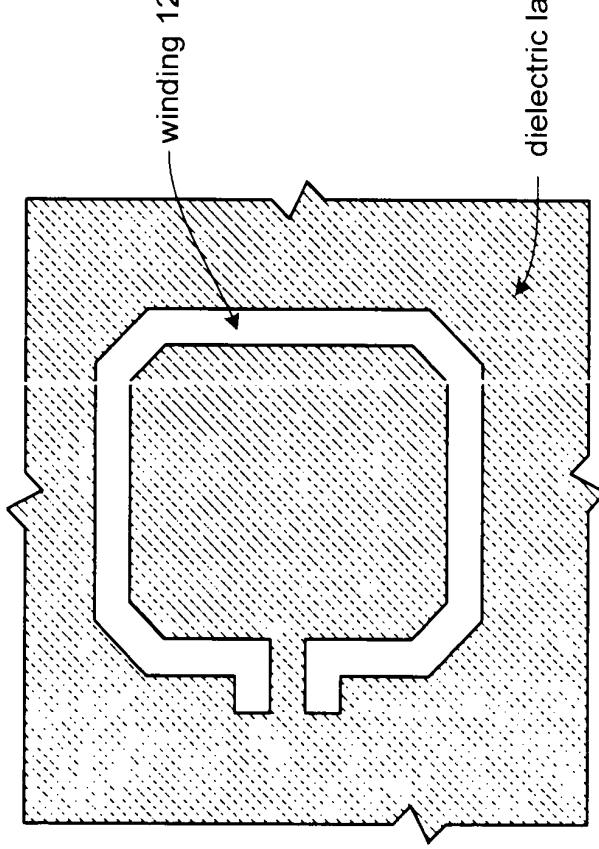


FIG. 3A
top view

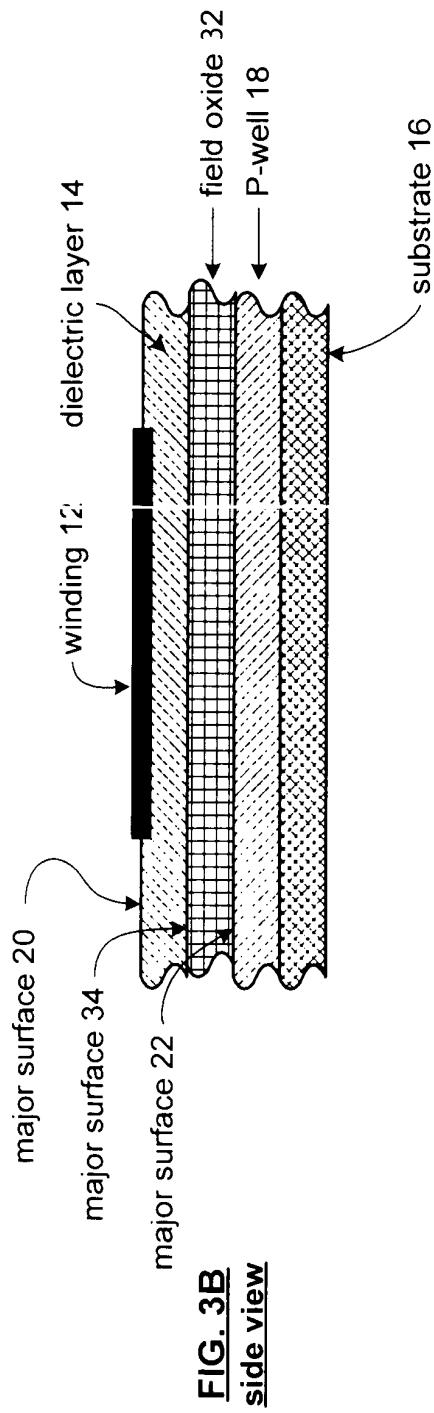


FIG. 3B
side view

on-chip inductor 40

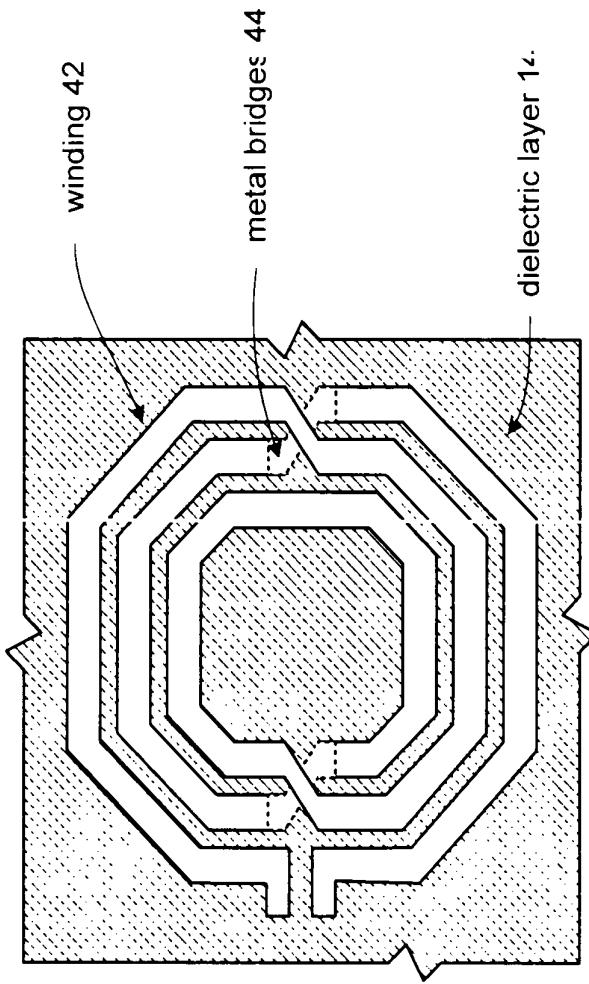


FIG. 4A
top view

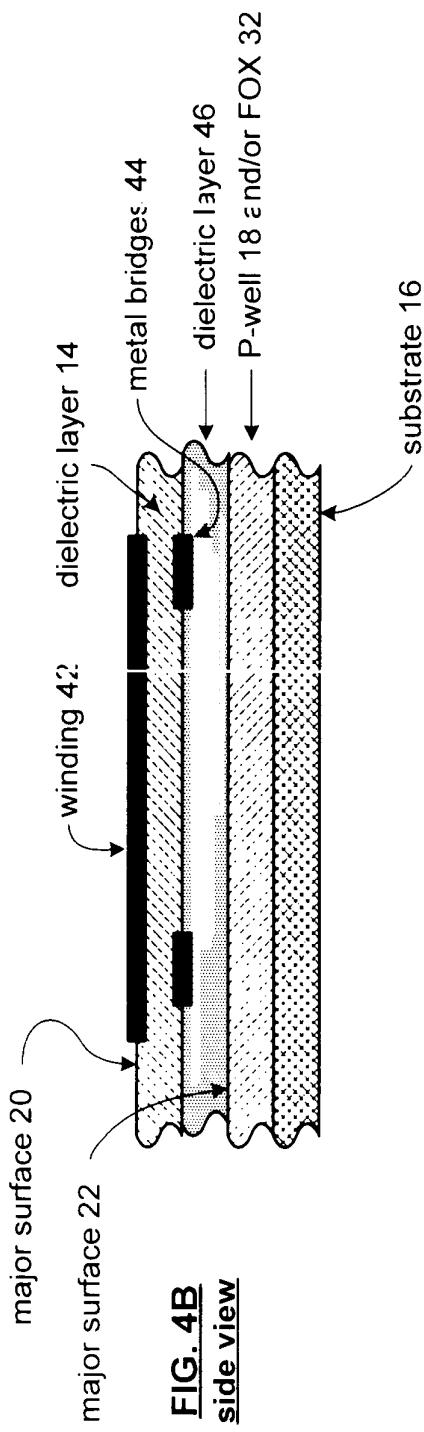


FIG. 4B
side view

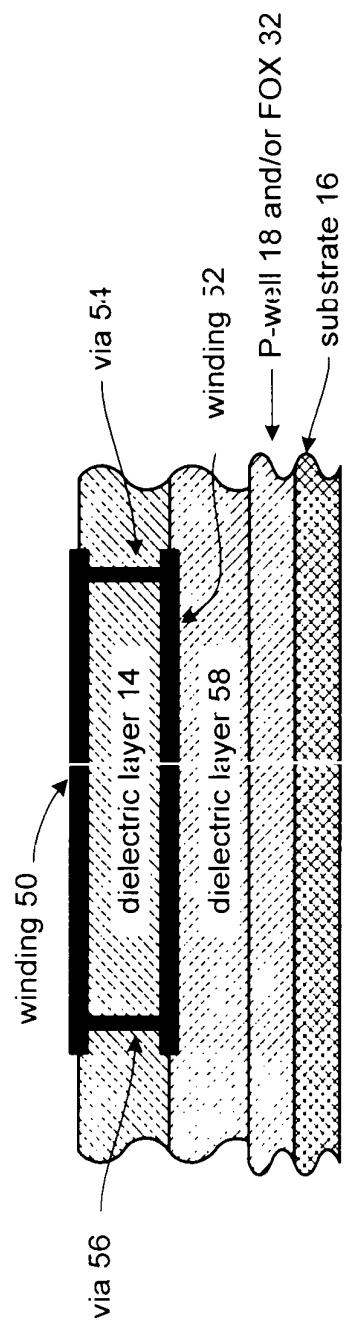


FIG. 5

differential inductor 60

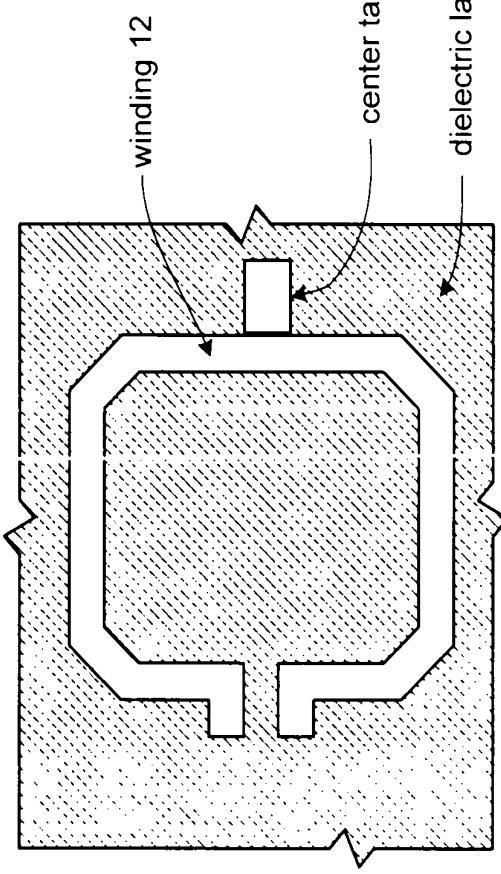


FIG. 6A
top view

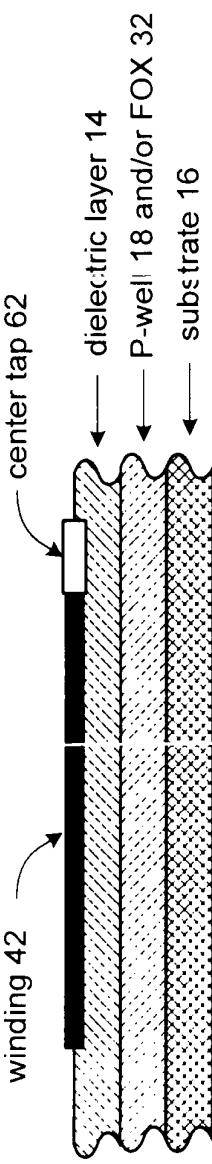


FIG. 6B
side view

FIG. 7C
bottcm view

dielectric layer 72
P-well 18 and/or FOX 32
substrate 16
secondary winding 74

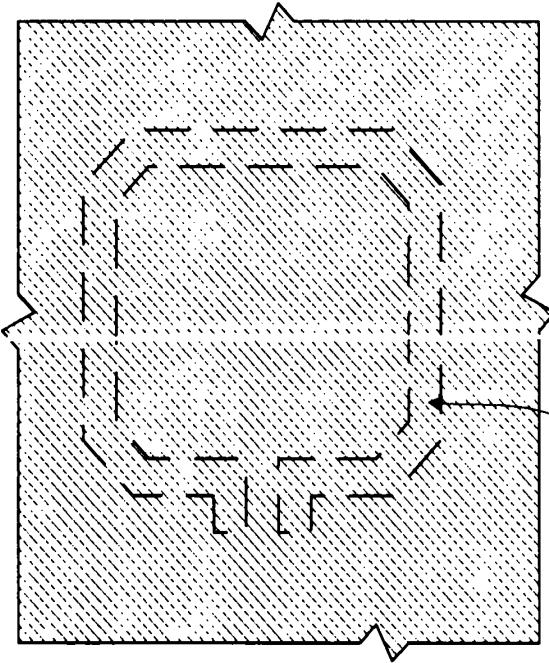


FIG. 7B
side view

winding 12
dielectric layer 14
secondary winding 74

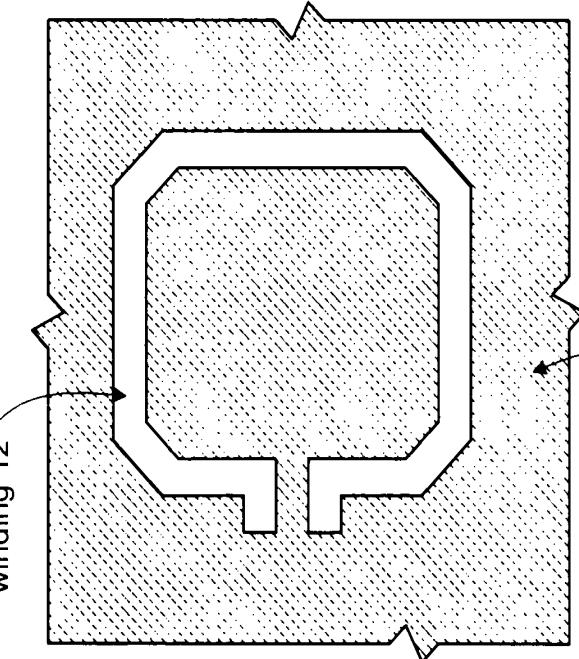
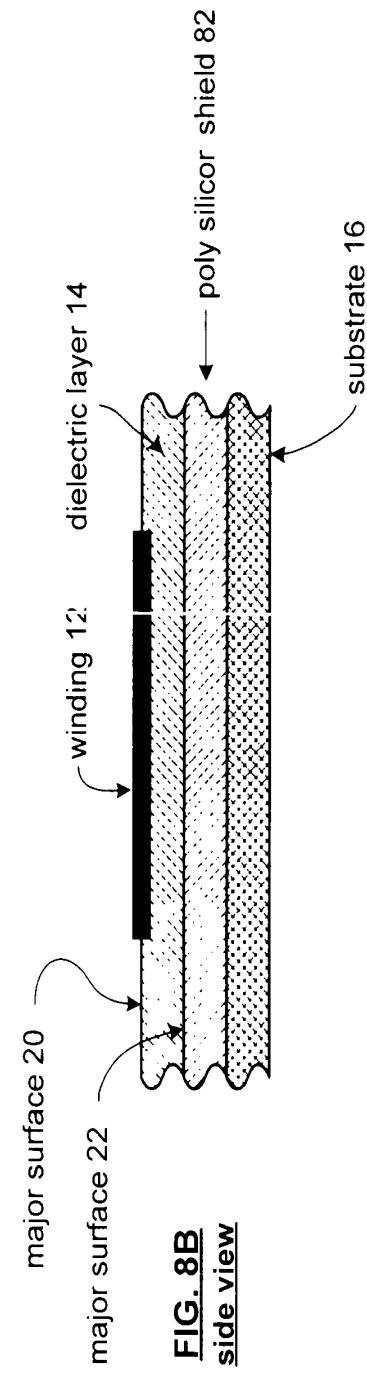
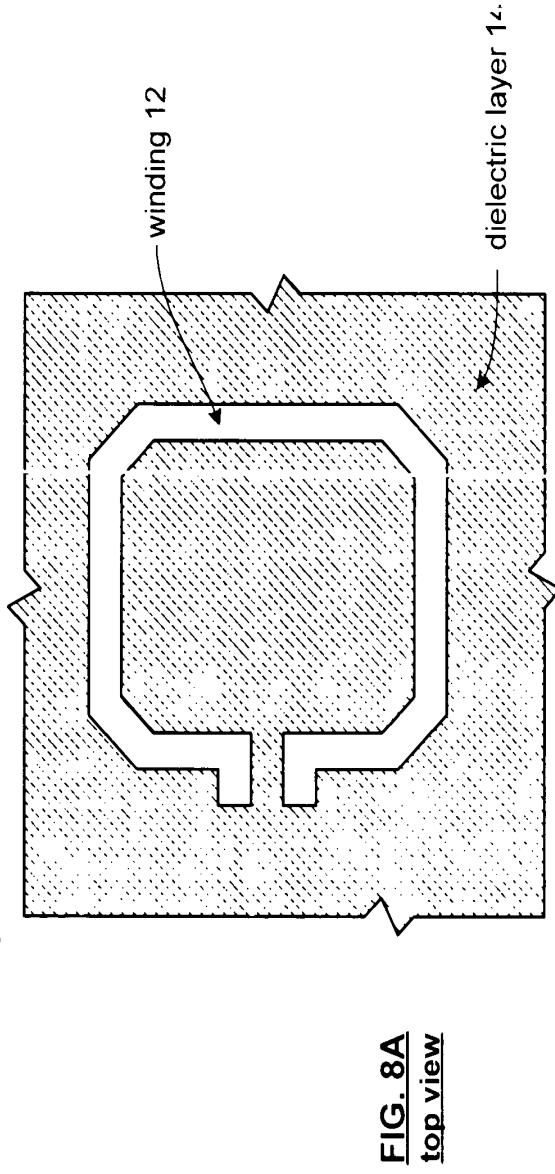


FIG. 7A
top view

winding 12
dielectric layer 14

on-chip transformer 70

on-chip inductor 80



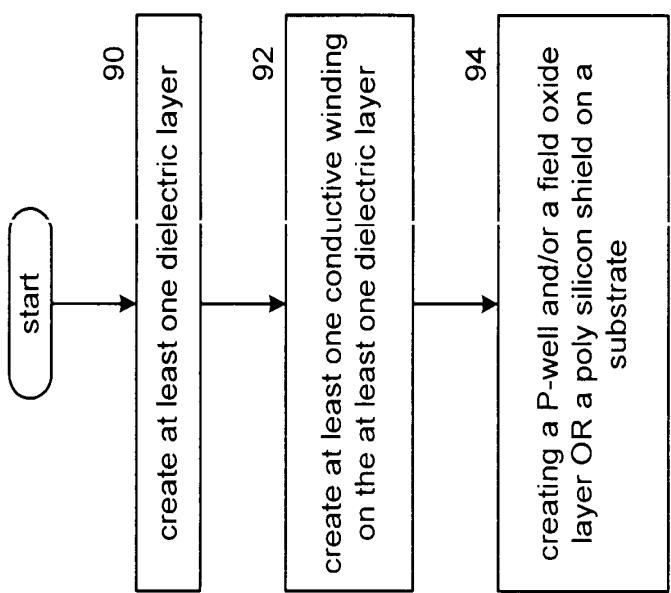


FIG. 9